

PATENT

Attorney Docket:  
33216M549654

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of: )  
                                  )  
                                  )  
**Kaoru Ishida, et al.**     )     Examiner: To Be Assigned  
                                  )  
Serial No.: To Be Assigned     )     Group Art Unit: To Be Assigned  
                                  )  
Filed:       Herewith         )  
For :                          POWER SPLITTER/COMBINER MULTI-LAYER CIRCUIT

**INFORMATION DISCLOSURE STATEMENT**

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicants are enclosing an Information Disclosure Citation Form (PTO-1449).

These documents were cited by the Applicants and the Examiner during prosecution of the parent application 10/102,732, filed March 22, 2002. No copies of the documents are necessary in accordance with 37 C.F.R. §1.98(d).

Applicants respectfully ask that each of these documents be made officially of record in this application, and that a listing of the same appear on the face of any patent which may issue from this application.

Respectfully submitted,  
SMITH, GAMBRELL & RUSSELL, LLP



Michael A. Makuch, Reg. No. 32,263  
1850 M Street, NW – Suite 800  
Washington, DC 20036  
Telephone : 202/263-4300  
Facsimile : 202/263-4329

August 22, 2003

FORM PTO-1449  INFORMATION DISCLOSURE STATEMENT		ATTY. DOCKET 33216M549654	SERIAL NO. To Be Assigned
		APPLICANT: Kaoru Ishida, et al.	
		FILING DATE Herewith	GROUP ART UNIT To Be Assigned

#### U.S. PATENT DOCUMENTS

*Examiner's Initials		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE, IF APPROPRIATE
	AA	5,017,886	5/21/91	Geller			
	AB	5,523,728	6/4/96	McCorkle			
	AC	5,497,137	3/5/96	Fujiki			
	AD	5,644,272	7/1/97	Dabrowski			
	AE	4,647,868	3/3/87	Mueller			
	AF	4,739,289	4/19/88	Cripps			
	AG	5,061,910	10/29/91	Bouny			
	AH	5,451,914	9/1995	Stengel			
	AI	4,755,775	7/1988	Marczewski et al.			
	AJ	6,201,439	3/2001	Ishida et al.			
	AK	2002/0008599	1/2002	Scidharan et al.			
	AL	5,467,064	11/1995	Gu			

#### FOREIGN PATENT DOCUMENTS

*Examiner's Initials		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO
	AM	5-191116	7/30/93	Japan			abstract
	AN	8-28607	3/21/96	Japan			X

#### OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, Etc.)

	AO	Takao HASEGAWA, et al., "Multi-Branch Power Dividers Using Multilayer MMIC Technology," IEICE Transactions on Electronics, Vol. E75-C, No. 6, June (1992), pp. 707-712.
	AP	Kenjiro NISHIKAWA, et al., "Miniaturized Wilkinson Power Divider Using Three-Dimensional MMIC Technology," IEEE Microwave and Guided Wave Letters, Vol. 6, No. 10, October, 1996, pp. 372-374.
	AQ	T. GOKDEMIR, et al, "Design and Performance of GaAs MMIC CPW Baluns Using Overlaid and Spiral Couplers," 1997 IEEE MTT-S International Microwave Symposium Digest, published 6/8/97, pp. 401-404.
	AR	Copy of European Communication for counterpart application no. EP 99 11 7393, dated April 19, 2001.
	AS	Dye, et al., "Radio Frequency Transistors," Butterworth/Heinemann, 1993, pp 113-116 and pp 179-181
	AT	Bassett, <i>Microwaves</i> , 19:7, 47-52, July 1990
	AU	European Search Report dated May 22, 2003 (Application No. EP 03 00 9622)

EXAMINER:	DATE CONSIDERED:
-----------	------------------

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.